

Notice of References Cited

Application/Control No.

09/776,387

Applicant(s)/Patent Under
Reexamination
PROTIGAL ET AL.

Examiner

A. Sefer

Art Unit

2826

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,656,605	04-1987	Clayton	365/52
	B	US-4,903,113	02-1990	Frankeny et al	357/70
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	N	JP 61-73367	04-1986	Japan	Matsumoto	
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NON-PATENT DOCUMENTS

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	U	"On-chip decoupling capacitor for logic VLSI chips," page 1280, IBM technical disclosure bulletin, Vol. 30 No. 3, Aug. 1987.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.